

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A high speed video mixer circuit, comprising:
a reference circuit generating a DC bias voltage; and
[[a]] an emitter-coupled differential pair receiving as input signals a video signal and said DC bias voltage, said differential pair providing a first output video signal corresponding substantially to an AC portion of said video signal, [[and]] said differential pair further providing a separate second output video signal amplifying said video signal about said DC bias voltage, whereby a voltage level of the first output video signal corresponds to a blanking signal for the second output video signal.
2. (Original) A high speed video mixer circuit as in Claim 1, further comprising:
a second reference circuit for generating a second bias voltage; and
a bias circuit biasing said first output video signal about said second bias voltage.
3. (Original) A high speed video mixer circuit as in Claim 1, further comprising a voltage clamp, said voltage clamp preventing an input transistor of said differential pair from going into saturation.
4. (Previously Presented) A high speed video mixer circuit, comprising:
a reference circuit generating a bias voltage;
a differential pair receiving as input signals a video signal and said bias voltage, said differential pair providing a first output video signal corresponding to said video signal, and a second output signal amplifying said video signal; and
a voltage clamp preventing an input transistor of said differential pair from going into saturation;

wherein said voltage clamp comprises a bipolar transistor having a base terminal receiving said bias voltage and having an emitter terminal coupled to a terminal of said input transistor, such that said terminal of said input transistor is clamped at substantially one base-emitter voltage drop above said bias voltage.

5. (Previously Presented) A high speed video mixer circuit, comprising:
an emitter-coupled amplifier receiving as input signals a first video signal and a bias voltage, said emitter-coupled amplifier providing a first output video signal corresponding to said first video signal, and a second output signal amplifying said first video signal; and

a blanking circuit receiving as input said first output video signal and a ground reference, said blanking circuit providing a blanking output signal dependent upon a voltage level of the first output video signal.

6. (Previously Presented) A high speed video mixer circuit as in claim 5, wherein:
said blanking circuit provides a blanking output signal without the input of an external mixing control signal.

7. (Previously Presented) A high speed video mixer circuit as in claim 5, wherein:
said first video signal includes a single channel from multiple channels of video information, and wherein said single channel can include any of said multiple channels.

8. (Previously Presented) A high speed video mixer circuit as in claim 5, further comprising:
an output stage receiving as input signals said second output signal, said blanking output signal, and a secondary video input signal, said output stage generating a final video output signal including a contribution from at least one of the second output signal and the secondary video input signal, the contribution dependent upon the blanking output signal.

9. (Previously Presented) A high speed video mixer circuit as in claim 8, wherein:
said secondary video input signal includes information for a single channel from multiple channels of secondary video input, and wherein the contribution from the secondary video input signal further includes information for each of said multiple channels.
10. (Previously Presented) A high speed video mixer circuit as in claim 5, wherein:
said first video signal includes an on-screen display input video signal.
11. (Previously Presented) A high speed video mixer circuit as in claim 8, wherein:
said secondary video input signal includes at least one of an analog video signal and a digital video signal.
12. (Previously Presented) A high speed video mixer circuit as in claim 5, further comprising:
a voltage clamp preventing an input transistor of said emitter-coupled amplifier from going into saturation.